

Yogendra Pratap Pundir

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Yogendra Pratap Pundir is Assistant Professor in Department of Electronics and Communication Engineering in School of Engineering and Technology at H. N. B. Garhwal Central University, Uttarakhand, India. He is pursuing PhD from NIT Uttarakhand. He has nine years of experience of teaching undergraduate students. Currently, he undertakes theory and laboratory courses related to electronic circuits and systems, VLSI design and Technology. Previously, he has worked as an R & D Engineer at Tejas Networks Pvt. Ltd., Bangalore, for 2 years. At Tejas Networks his primary role was of Board Designer. He is a post graduate in Microelectronic Systems (batch 2005-07), from Indian Institute of Science, Bangalore. He graduated from Hemvati Nandan Bahuguna Garhwal University in 2004 with Honors in Bachelor of Technology degree. He qualified the GATE-2004 examination with 99.94% and secured 3rd rank at all India level. During his education he received various scholarships from government. His technical expertise includes Board Design, FPGA based systems and IC Design.

- Specialization at Post graduate Level** : Microelectronic Systems.
- Qualification** : B. Tech., M. E., PhD (pursuing).
- Research Interests** : Reconfigurable logics and Circuits, Integrated Circuits.
- Under Graduate Courses taught** : Basic Electronics, Electronic Devices and Circuits, Computer System Architecture, VLSI Technology, Microcontrollers, Analog Integrated Circuits
- Other roles at current university** : Head (Dept. of ECE).